

## Preamble

A substantial fraction of the national growth of the 21st century has been derived from advances in science and technology and their applications in manufacturing, infrastructure, energy, health, environment, information and communication technologies, resource management and many other sectors through mastering the adoption, adaptation and advancement of existing technologies, as well as the creation of new ones. Innovation refers to the full range of changes, large and small, used to achieve desired outcomes and innovation often results from basic research that can lead to ground breaking discoveries and inventions.

The cardinal point of importance of the conference aims at enriching and enhancing the quality of innovation research of the issues at the core of national growth through developments in the sectors of science and technology by managing those applying modern scientific management techniques and conveying them through effective and efficient communication skills. Innovations will be essential in combating the challenges which cause hindrances to National Growth. The proposed conference will put an honest attempt to meet up the expectations of the common people in developing the nation through innovations.

## The Institute

Bhilai Institute of Technology initiated by few notable stalwarts of central India in 1986 has now evolved as one of the premier Technical and Management Institute of this part of the country imparting quality education and providing environment for academic research. BIT campus has a conducive and supportive academic ambience facilitating high quality education and research for about 3000 students of graduate, post graduate and doctoral programmes. Five of its undergraduate programs have been accredited once gain in 2017 by the National Board of Accreditation (NBA) and the institute itself has been A grade accredited by the National Accreditation and Assessment Council (NAAC) in 2017. The institute stands in 150-200 NIRF rank - band in the category of Engineering in 2018 rankings. The institute has an installation of ISO 9001:2015

BIT is well connected through Rail network as well as Air network of the country. BIT is at about 1km distance from the Durg Railway station on Howrah-Mumbai line and is about 45 km away from the Swami Vivekananda Airport at Raipur, the capital city of Chhattisgarh state.

## The Department

The Department of Electrical and Electronics Engineering was incepted in the year 2007. The department offers an effective curriculum which is a judicious mixture of Electrical Engineering and Electronics Engineering. This course enables students to imbibe concepts of two vital fields of technology in one course. The Vision of the department is 'To impart education and transform students into competent professionals in Electrical and Electronics Engineering to excel in various challenges to serve the society'. The Mission of the department is, 'To provide excellent infrastructure, competent faculty members, disciplined ambience with rich ethical values, to assimilate Engineering knowledge and transform into efficient professionals by inculcating perpetuated learning to cope with the challenges in research and industry.

## Conference Objective

The main objective of this National Conference is to comprehend today's scenario and to explore the futuristic trends in the field of Electrical and Electronics Engineering. It also provides a platform to the people from academia and industry to discuss their research results and findings, practical challenges and solutions in the various domains of their interest in the context of Electrical and Electronics Engineering

## Themes and Sub Themes

Futuristic Trends in Electrical and Electronics Engineering

Sub themes :

- Advanced Control Systems
- Electric drives and applications
- power system modeling and control
- HVDC and FACTS
- Power Quality and Security
- Renewable energy Sources
- Industrial Process Control and automation
- Advances in Communication systems
- Microcontrollers and Embedded systems
- High voltage engineering
- Bio-Medical Circuits and Systems
- Signal Processing
- IOT
- PLC and instrumentation
- Applications of soft computing techniques in EEE
- Applications of optimization in EEE

# BITCON 2019

*Futuristic Trends in Science, Technology & Management for National Development*

**NATIONAL CONFERENCE  
ON**

**Futuristic Trends in Electrical  
and Electronics Engineering for  
National Development**

*Conducted By*

*Department of Electrical and  
Electronics Engineering*

**1<sup>st</sup> March 2019**

## REGISTRATION FORM

Name : .....

Designation : .....

Organization : .....

Mailing Address : .....

Mobile : .....

E-mail : .....

Phone : .....

## Details of Registration Fees:

DD Drawn on:

Amount (in Rs.):

Demand Draft No. :

Date:

**Date:**

**Place:**

**Signature of Applicant**

The applicant is sponsored for the conference. He/She is permitted to attend the conference.

**Date:**

**Place:**

**Signature and Seal of  
Sponsoring Authority**

### Chief Patrons

<b>Shri Vijay Kumar Gupta</b>	Chairman (Governing Body), BIT Trust
<b>Shri I.P. Mishra</b>	Ex-GM, Bhilai Steel Plant & Member Secretary, BIT Trust

### Patron

<b>Dr. Arun Arora</b>	Principal, BIT, Durg.
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### Co-ordinator

<b>Dr. M.V. Padmavati</b>	Dean (R&D), BIT, Durg.
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### Advisory Committee

<b>Dr. M.K. Verma</b>	Vice Chancellor, CSVTU, Bhilai
<b>Prof. Shailendra Saraf</b>	Vice Chancellor, Durg University
<b>Dr. R.P. Dubey</b>	Vice Chancellor, CVRU, Bilaspur
<b>Dr. Deepankar Choudhary</b>	Professor, IIT Mumbai
<b>Dr. R.L. Shrivastava</b>	Professor, YCCE Nagpur
<b>Dr. K.K. Ghosh</b>	Professor, RSU Raipur
<b>Dr. S.P.S. Rajpoot</b>	Professor, MNIT Bhopal
<b>Dr. G.V.V. Sharma</b>	Professor, IIT Hyderabad
<b>Dr. Deepak Garg</b>	Professor, Bennett University, Noida
<b>Dr. Tassanee Prueksasit</b>	Professor, Chulalongkorn University, Thailand
<b>Dr. S.K. Ganguly</b>	Dean (Quality Management), BIT, Durg
<b>Dr. Manisha Sharma</b>	Dean (Academics), BIT, Durg
<b>Dr. Mohan Gupta</b>	Dean (Infrastructure Development), BIT, Durg
<b>Dr. Santosh Sar</b>	Dean (Students Welfare), BIT, Durg
<b>Dr. Anup Mishra</b>	Dean (Administration), BIT, Durg

### Coordinating Committee

<b>Dr. Manoj Pal</b>	<b>Mr. Vikas Pandey</b>
<b>Mr. Mousam Sharma</b>	<b>Mr. Pawan Patnaik</b>
<b>Dr. Sumita Nair</b>	<b>Mr. Prashant Sahu</b>
<b>Dr. Anjana Shekhar</b>	<b>Dr. Anindita Chakraborty</b>
<b>Mr. Amit Biswas</b>	<b>Mr. Hitesh Sharma</b>
<b>Dr. Arun Sahu</b>	<b>Dr. Daljeet Singh Wadhwa</b>
<b>Dr. G.C. Biswal</b>	

### Convener

<b>Dr. Anup Mishra</b>	HOD (Electrical and Electronics Engineering)
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### Organizing Secretary

<b>Dr. (Mrs.) Surekha Bhusnur</b>	Professor, EEE
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### Organizing Committee

<b>Dr. Abhishek Verma</b>	<b>Mrs. Naushin Anjum</b>
<b>Mr. Mousam Sharma</b>	<b>Mr. Brahma Nand Thakur</b>
<b>Mrs. Alka Mishra</b>	
<b>Mr. Abhijeet Lal</b>	
<b>Mr. Abhishek Agrawal</b>	
<b>Mr. Mukesh Chandrakar</b>	

### Important Dates

Submission of full length papers	: 15 <sup>th</sup> February 2019
Intimation of acceptance deadline	: 17 <sup>th</sup> February 2019
Receipt of registration fees	: 20 <sup>th</sup> February 2019

### Registration Fees

Research Scholar/PG student	: Rs. 500/-
Faculty Member	: Rs. 800/-
Professional from Industry	: Rs. 1000/-
UG Student	: Rs. 300/-

1. All the payments should be made by a crossed Demand Draft in favor of "Bhilai Institute of Technology" payable at "Durg".
2. NEFT or at Bank Counter

**Beneficiary Account Number** : 32471126372

**Beneficiary Name** : Bhilai Institute of Technology, Durg

**Bank** : State Bank of India, Sector-10, Bhilai

**Address** : Alka Departmental Stores, Sector-10, Bhilai (C.G.)

**IFSC Code** : SBIN0006942

*(No TADA will be paid from the Institute)*

### Address for Correspondence

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### Eligibility

Academicians, Consulting Engineers, R&D Institutes, Government Departments and Agencies, Private Companies, Research Scholars and P.G. Students.

### Call for Papers

Selected papers will be published in indexed Journals. Prospective authors are requested to submit their full papers in MS Word format by E-mail to the addresses of correspondence. Please find the attached template for further preparation of manuscript at [www.bitdurg.ac.in/bitcon2019](http://www.bitdurg.ac.in/bitcon2019).

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*Futuristic Trends in Science, Technology &  
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**NATIONAL CONFERENCE**

**ON**

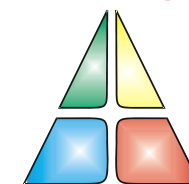
**Futuristic Trends in Electrical  
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**1<sup>st</sup> March 2019**



*Organized By*

**Department of Electrical and  
Electronics Engineering**



**ASPIRE TO EXCEL**

**BHILAI INSTITUTE OF TECHNOLOGY, DURG**

(Seth Balkrishan Memorial)

Accredited with 'A' Grade by NAAC

NIRF 17-18 RANK-BAND: 151-200

An ISO 9001:2015 CERTIFIED INSTITUTION

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